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NOR 01 Introduction

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(b) By using the gate
array platform given in
Fig. 1.30, implement the
CMOS circuit as
compactly as possible

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with the aspect ratio,
which is the ratio of
vertical dimension to
horizontal dimension, as
close to 1 as possible.

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Solved > 6.2 Consider
switching delays for 10

fF in a from ...

SOLUTION : (a) When

$V_{out} = V_{OL}$, $V_{in} =$

$V_{DD} = 1.2V$, the

driver transistor

operates in linear

region. Using Eq.(5.12)

Since V_{OL} is small, $V_{OL} / E_c L_n$ can be

ignored. Using (5. 17)

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Solve for W/L , (b)

When $V_{in} = V_{IL}$,
driver transistor

operates in saturation

region. Since V_{in} is

slightly higher than V_{T0} ,

$V_{in} - V_{T0}$ can be

ignored. Also $v_{sat} = E$

$C / 2$. Using Eq.(5. 21)

Solved > 5.1 Design a

resistive-load inverter

with $R = 2 \dots$

with the design,

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simulation and layout of CMOS analog and digital integrated circuits. Some

outstanding features of the text are; 1) Software for layout of integrated circuits 2) Information and directions on submitting chips to MOSIS 3) tutorial presentation of material suitable for self study or as a University

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focuses on the

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platforms. Research
spans the analysis,
design, simulation, and
validation of analog,

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mixed-mode, (sub) mm-wave, RF, power, and digital circuits, and their applications from computation and sensing to cyber-physical and implantable biomedical systems.

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Circuits, such as logic circuits, memories, and microprocessors which operate with electrical signals representing ones and zeroes, each mosfet behaves primarily as a switch, with its gate serving to open and close a channel connecting its source and drain.

Highly conductive

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composite polysilicon
gate for CMOS ...

Some ICs combine
analog and digital

MOSFET circuitry on a
single mixed-signal
integrated circuit,

making the needed
board space even

smaller. This creates a
need to isolate the

analog circuits from the
digital circuits on a chip
level, leading to the use

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of isolation rings and
silicon on insulator
(SOI). Since MOSFETs
require more space to ...

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